ABSTRACT OF THE DISCLOSURE

The number of pulses of an operation clock to a microprocessor can be easily and instantaneously controlled and changed, in which a clock control device 2 supplies a clock of the same pulse number as the system clock 8, while a bus busy signal 10 that indicates a bus 4 is in a busy state from a microprocessor 1 is existing, and supplies a clock having random gap of a reduced number of pulses of the system clock 8, while the bus busy signal 10 is not existing, respectively to the microprocessor 1 as operation clock.